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JUL 31 2002
500.26967RC1
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Applicant(s): KATSURA, et al

Serial No.: 09/536,646

Filed: March 28, 2000

For: GRAPHIC PROCESSING APPARATUS UTILIZING IMPROVED DATA
TRANSFER TO REDUCE MEMORY SIZE

Group: 2671

Examiner: U. Chauhan

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SUBMISSION OF SUPPLEMENTAL REISSUE DECLARATION

Assistant Commissioner
for Patents
Washington, DC 20231

July 30, 2002

Sir:

Applicants are submitting herewith a Supplemental Reissue Declaration By the Inventor wherein the residence of the second and third inventors have been corrected. Entry of the Supplemental Reissue Declaration by the Inventor is respectfully requested.

To the extent necessary, applicants petition for an extension of time under 37 C.F.R. section 1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 01-2135 (Case No. 500.26967RC1) and please credit any excess fees to such Deposit Account.

Respectfully submitted,

703/312-6600
CIB/jdc

Carl I. Brundidge
Registration No. 29,621
ANTONELLI, TERRY, STOUT & KRAUS, LLP

EA



SUPPLEMENTAL
REISSUE APPLICATION DECLARATION BY THE INVENTOR

Docket Number (Optional)
500.26967RC1

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As a below named inventor, I hereby declare that:

My residence, mailing address and citizenship are stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is described and claimed in patent number 4,975,857 granted December 4, 1990, and for which a

reissue patent is sought on the invention entitled Graphic Processing Apparatus Utilizing Improved Data Transfer to Reduce Memory Size the specification of which

☐ is attached hereto.

☒ was filed on March 28, 2000 as reissue application number 09 / 536,646 and was amended on 3/28/00; 6/1/00 & 4/24/02 (If applicable)

I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56.

I verily believe the original patent to be wholly or partly inoperative or invalid, for the reasons described below. (Check all boxes that apply.)

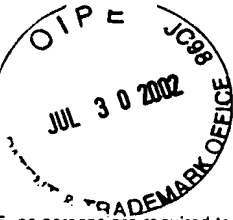
☒ by reason of a defective specification or drawing.

☒ by reason of the patentee claiming more or less than he had the right to claim in the patent.

☒ by reason of other errors. (See attachment)

At least one error upon which reissue is based is described below. If the reissue is a broadening reissue, such must be stated with an explanation as to the nature of the broadening:

[Page 1 of 2]



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(REISSUE APPLICATION DECLARATION BY THE INVENTOR, page 2)

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500.26967RC1

All errors corrected in this reissue application arose without any deceptive intention on the part of the applicant. As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the United States Patent and Trademark Office connected therewith.

Name(s) Registration Number

See Attached List of Attorneys

Correspondence Address: Direct all communications about the application to:

☒ Customer Number

020457

Type Customer Number here

Place Customer Number Bar
Code Label here

☐ Firm or
Individual Name

Address

Address

City

State

Zip

Country

Telephone

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine and imprisonment, or both, under 18 U.S.C. 1001, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this declaration is directed.

Full name of sole or first inventor (given name, family name)

Koyo KATSURA

Inventor's signature

Date

7/1/02

Residence

Hitachiota-shi, Japan

Citizenship

Japan

Mailing Address c/o Hitachi, Ltd., Intellectual Property Group
New Marunouchi Bldg., 5-1 Marunouchi 1-chome, Chiyoda-ku, Tokyo, JAPAN

Full name of second joint inventor (given name, family name)

Shinichi KOJIMA

Inventor's signature

Date

7/1/02

Residence

Takasaki-shi, Japan

Citizenship

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Mailing Address c/o Hitachi, Ltd., Intellectual Property Group
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Full name of third joint inventor (given name, family name)

Noriyuki KURAKAMI

Inventor's signature

Date

7/1/02

Residence

Maebashi-shi, Japan

Citizenship

Mailing Address c/o Hitachi, Ltd., Intellectual Property Group
New Marunouchi Bldg., 5-1 Marunouchi 1-chome, Chiyoda-ku, Tokyo, JAPAN

☐ Additional joint inventors are named on separately numbered sheets attached hereto.

Attachment to PTO/SB/151 (42-97) Reissue Application Declaration by
the Inventor

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I hereby delete that:

Every error in the patent which was corrected in the present Reissue application, and which is not covered by the prior Reissue Declaration submitted in this application, arose without any deceptive intention on the part of the Applicants.

At least one error upon which reissue is based is described as follows:

Specifically, during review of the specification and claims of U.S. Patent No. 4,975,857, subsequent to its issuance, undertaken as a result of discussions with another party concerning the technology and that party, it was found that the claims thereof were unduly limited in that they called for limitations not necessary to the patentable invention as disclosed.

More particularly, it was discovered that an error arose during the prosecution of U.S. application Serial No. 07/302,332, filed January 27, 1989 from which U.S. Patent No. 4,975,857 issued due to the failure of the inventors and that of the Japanese Agent to fully appreciate and recognize that the invention could have been claimed more broadly. Specifically, the error occurred due to the fact that the claims could have been broadened to recite a memory controller not including various features such as "multiplexer means", to recite that the output means is a digital to analog converter and to recite that the output means is at least one output terminal included in the memory controller.

CLAIM OF PRIORITY:

Under the provision of title 35, United States Code, Section 119, we reaffirm our claim for priority based upon Japanese Patent Application No. 63-93448, filed in Japan on April 18, 1988, a certified copy of which document and a claim for priority based thereon having been filed in U.S. Application Serial No. 07/302,332, filed January 27, 1989, which matured as U.S. Patent No. 4,975,857.

Under the provision of title 35, United States Code, Section 120, we hereby claim the benefit of U.S. Reissue application Serial No. 07/985,141, filed December 3, 1992, with respect to U.S. application Serial No. 07/302,332, filed January 27, 1989, now U.S. Patent No. 4,975,857 upon which the present Reissue application has been filed, insofar as the subject matter of each of the claims of the present Reissue application is not disclosed in the prior U.S. applications in the manner provided by the first paragraph of title 35, United States Code, Section 112, we hereby acknowledge the duty to disclose material information as defined in title 37, Code of Federal Regulation Section 1.56 which occurred between the filing of the prior U.S. application and the filing of the present Reissue application.